



Form C: Type Test Verification Report

Type Approval and Manufacturer declaration of compliance with the requirements of G98/NI..

This form should be used when making a Type Test submission to the Energy Networks Association (ENA).

If the **Micro-generator** is **Fully Type Tested** and already registered with the ENA **Type Test Verification Report** Register, the **Installation Document** should include the **Manufacturer**'s Reference Number (the Product ID), and this form does not need to be submitted.

Where the **Micro-generator** is not registered with the ENA **Type Test Verification Report** Register this form needs to be completed and provided to the **DNO**, to confirm that the **Micro-generator** has been tested to satisfy the requirements of this EREC G98/NI..

Manufacturer's reference number		MIN 3600TL-XH.				
Micro-generator technology		MIN 2500TL-XH, MIN 3000TL-XH, MIN 3600TL-XH.				
Manufactur	er name		Shenzhen	Growatt New E	Energy Co., Ltd.	
Address			4-13th Floor, Building A, Sino-German Europe Industrial Demonstration Park, No. 1, Hangcheng Avenue, Bao'an District, Shenzhen, Guangdong, China.			
Tel	+86 755 29	51 5888	Fax		+86 755 2747 2131	
E-mail	Peng.zhu@	growatt.com	Web site www.ginverter.com		www.ginverter.com	
		Connection (Option			
Registered use separate	e sheet if	2.5-3.6	kW single phase, single, split or three phase system			
	more than one connection option.		kW three phase			
N/A		N/A	kW two phases in three phase system			
N/A		kW two phases split phase system				
Manufactur	T T.		Leartify that all products augusted by the company with the above			

Manufacturer Type Test declaration. - I certify that all products supplied by the company with the above **Type Tested** reference number will be manufactured and tested to ensure that they perform as stated in this document, prior to shipment to site and that no site modifications are required to ensure that the product meets all the requirements of EREC G98/NI..

Signed		On behalf of	Shenzhen Growatt New Energy Co., Ltd.
	Jeng Ihu		Go., Ltd.

Note that testing can be done by the **Manufacturer** of an individual component or by an external test house.

Where parts of the testing are carried out by persons or organisations other than the **Manufacturer** then that person or organisation shall keep copies of all test records and results supplied to them to verify that the testing has been carried out by people with sufficient technical competency to carry out the tests.



1.Operating Range: This test should be carried out as specified in EN 50438 D.3.1.

Active Power shall be recorded every second. The tests will verify that the **Micro-generator** can operate within the required ranges for the specified period of time.

The Interface Protection shall be disabled during the tests.

In case of a PV Micro-generator the PV primary source may be replaced by a DC source.

In case of a full converter **Micro-generator** (eg wind) the primary source and the prime mover **Inverter**/rectifier may be replaced by a **DC** source.

In case of a DFIG **Micro-generator** the mechanical drive system may be replaced by a test bench motor.

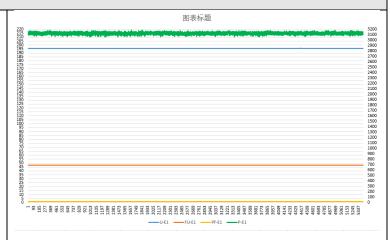


Voltage = 85% of nominal (195.5 V)

Frequency = 47.5 Hz

Power factor = 1

Period of test 90 minutes



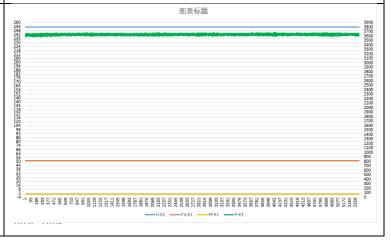
Test 2

Voltage = 110% of nominal (253 V).

Frequency = 51.5 Hz

Power factor = 1

Period of test 90 minutes





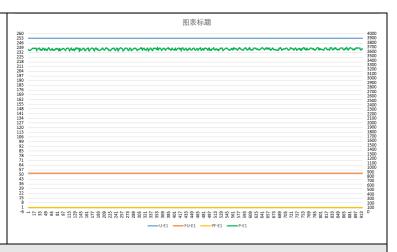
Test 3

Voltage = 110% of nominal (253 V).

Frequency = 52.0 Hz

Power factor = 1

Period of test 15 minutes



2.Power Quality – Harmonics: These tests should be carried out as specified in BS EN 61000-3-2. The chosen test should be undertaken with a fixed source of energy at two power levels a) between 45 and 55% and b) at 100% of Registered Capacity. The test requirements are specified in Annex A1 A.1.3.1 (Inverter connected) or Annex A2 A.2.3.1 (Synchronous).

Micro-generator tested to BS EN 61000-3-2

Micro-g	Micro-generator rating per phase (rpp)		3.6 kW		NV=MV*3.68/rpp	
Harmonic	Harmonic At 45-55% of Registered Capacity		100% of Registered Capacity			
	Measured Value MV in Amps	NV	Measured Value MV Amps	in NV	Limit in BS EN 61000- 3-2 in Amps	Higher limit for odd harmonics 21 and above
2	0.0074	0.0076	0.0075	0.0077	1.080	
3	0.0798	0.0816	0.0590	0.0603	2.300	
4	0.0040	0.0041	0.0040	0.0041	0.430	
5	0.0283	0.0289	0.0449	0.0459	1.140	
6	0.0024	0.0025	0.0026	0.0027	0.300	
7	0.0330	0.0337	0.0434	0.0444	0.770	
8	0.0032	0.0033	0.0031	0.0032	0.230	
9	0.0291	0.0297	0.0335	0.0342	0.400	
10	0.0021	0.0021	0.0024	0.0025	0.184	
11	0.0191	0.0195	0.0221	0.0226	0.330	
12	0.0017	0.0017	0.0017	0.0017	0.153	



13 0.0148 0.0151 0.0160	0.0164 0.210
14 0.0013 0.0013 0.0013	0.0013 0.131
15 0.0081 0.0083 0.0111	0.0113 0.150
16 0.0008 0.0008 0.0009	0.0009 0.115
17 0.0056 0.0057 0.0086	0.0088 0.132
18 0.0007 0.0007 0.0009	0.0009 0.102
19 0.0043 0.0044 0.0071	0.0073 0.118
20 0.0006 0.0006 0.0008	0.0008 0.092
21 0.0036 0.0037 0.0051	0.0052 0.107 0.160
22 0.0005 0.0005 0.0006	0.0006 0.084
23 0.0026 0.0027 0.0039	0.0040 0.098 0.147
24 0.0004 0.0004 0.0005	0.0005 0.077
25 0.0017 0.0017 0.0033	0.0034 0.090 0.135
26 0.0004 0.0004 0.0005	0.0005 0.071
27 0.0018 0.0018 0.0026	0.0027 0.083 0.124
28 0.0003 0.0003 0.0004	0.0004 0.066
29 0.0013 0.0013 0.0021	0.0021 0.078 0.117
30 0.0003 0.0003 0.0004	0.0004 0.061
31 0.0011 0.0011 0.0016	0.0016 0.073 0.109
32 0.0003 0.0003 0.0003	0.0003 0.058
33 0.0009 0.0009 0.0013	0.0013 0.068 0.102
34 0.0002 0.0002 0.0003	0.0003 0.054
35 0.0008 0.0008 0.0013	0.0013 0.064 0.096
36 0.0002 0.0002 0.0003	0.0003 0.051
37 0.0007 0.0007 0.0014	0.0014 0.061 0.091
38 0.0002 0.0002 0.0002	0.0002 0.048
39 0.0006 0.0006 0.0012	0.0012 0.058 0.087



40	0.0002	0.0002	0.0002	0.0002	0.046	

Note the higher limits for odd harmonics 21 and above are only allowable under certain conditions, if these higher limits are utilised please state the exemption used as detailed in part 6.2.3.4 of BS EN 61000-3-2 in the box below.

3.Power Quality – Voltage fluctuations and Flicker: These tests should be undertaken in accordance with EREC G98/NI Annex A1 A.1.3.3 (**Inverter** connected) or Annex A2 A.2.3.3 (Synchronous).

	Starting				Stopping			Running			
	d max	d c	d(t)		d max	d c	d(t)		P _{st}		P _{lt} 2 hours
Measured Values at test impedance	1.03	0.21	0		1.07	0.28	0		0.23		0.22
Normalised to standard impedance	1.03	0.21	0		1.07	0.28	0		0.23		0.22
Normalised to required maximum impedance											
Limits set under BS EN 61000- 3-11	4%	3.3%	3.3%		4%	3.3%	3.3%	1	1.0		0.65
Test Impedance	R	0.4		Ω		Х		0.2	25	Ω	
Standard Impedance	R	0.24 * 0.4 ^		Ω		X			15 * 25^	Ω	
Maximum Impedance	R	-		Ω		Х				Ω	

Applies to three phase and split single phase **Micro-generators**.

For voltage change and flicker measurements the following formula is to be used to convert the measured values to the normalised values where the power factor of the generation output is 0.98 or

[^] Applies to single phase **Micro-generators** and **Micro-generators** using two phases on a three phase system.



above.

Normalised value = Measured value*reference source resistance/measured source resistance at test point.

Single phase units reference source resistance is 0.4 Ω

Two phase units in a three phase system reference source resistance is $0.4~\Omega$.

Two phase units in a split phase system reference source resistance is 0.24 Ω .

Three phase units reference source resistance is 0.24Ω .

Where the power factor of the output is under 0.98 then the X to R ratio of the test impedance should be close to that of the Standard Impedance.

The stopping test should be a trip from full load operation.

The duration of these tests need to conform to the particular requirements set out in the testing notes for the technology under test. Dates and location of the test need to be noted below.

Test start date	28,Sep,2022	Test end date	28,Sep,2022		
Test location	Growatt certified testing laboratory				

4.Power quality – DC injection: This test should be carried out in accordance with EN 50438 Annex D.3.10

D.3.10				
Test power level (3.6K)	20%	50%	75%	100%
Recorded value in Amps	17.22mA	20.35 mA	25.04mA	29.74mA
as % of rated AC current	0.11%	0.13%	0.16%	0.19%
Limit	0.25%	0.25%	0.25%	0.25%
Test power level (3K)	20%	50%	75%	100%
Recorded value in Amps	13.04mA	16.96mA	19.57mA	22.17mA
as % of rated AC current	0.10%	0.13%	0.15%	0.17%
Limit	0.25%	0.25%	0.25%	0.25%
Test power level (2.5K)	20%	50%	75%	100%
Recorded value in Amps	10.87mA	13.04 mA	16.30mA	17.39mA
as % of rated AC current	0.10%	0.12%	0.15%	0.16%
Limit	0.25%	0.25%	0.25%	0.25%



5.Power Quality – Power factor: This test shall be carried out in accordance with EN 50548 Annex D.3.4.1 but with nominal voltage -6% and +10%. Voltage to be maintained within $\pm 1.5\%$ of the stated level during the test.

	216.2 V	230 V	253 V
20% of Registered Capacity	0.96117	0.96089	0.96048
50% of Registered Capacity	0.98684	0.98596	0.98759
75% of Registered Capacity	0.99271	0.99255	0.99304
100% of Registered Capacity	0.99683	0.99642	0.99691
Limit	>0.95	>0.95	>0.95

6.Protection – Frequency tests: These tests should be carried out in accordance with EN 50438 Annex D.2.4 and the notes in EREC G98/NI Annex A1 A.1.2.3 (**Inverter** connected) or Annex A2 A.2.2.3 (Synchronous)

Function	Setting		Trip test		"No trip tests"	
	Frequency	Time delay	Frequency	Time delay	Frequency /time	Confirm no trip
U/F	48.0 Hz	0.5 s	47.99Hz	0.516s	48.2Hz 25 s	No trip
					47.8 Hz 0.45s	No trip
O/F	52 Hz	1.0 s	52.01Hz	1.019s	51.8 Hz 120s	No trip
					52.2 Hz 0.98 s	No trip

Note. For frequency trip tests the frequency required to trip is the setting \pm 0.1 Hz. In order to measure the time delay a larger deviation than the minimum required to operate the projection can be used. The "No trip tests" need to be carried out at the setting \pm 0.2 Hz and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

7.Protection – Voltage tests: These tests should be carried out in accordance with EN 50438 Annex D.2.3 and the notes in EREC G98/NI Annex A1 A.1.2.2 (**Inverter** connected) or Annex A2 A.2.2.2 (Synchronous)

Function	Setting	Trip test	"No trip tests"
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	Voltage	Time delay	Voltage	Time delay	Voltage /time	Confirm no trip
U/V stage 1	195.5 V	3 s	194.92V	3 .018s	199.5 V 5 s	No trip
U/V stage 2	138 V	2 s	137.51V	2 .020s	142 V 2.5 s	No trip
					134 V 1.98 s	No trip
O/V	253 V	0.5 s	253.79V	0.519s	249 V 5 s	No trip
					257 V 0.45 s	No trip

Note for Voltage tests the Voltage required to trip is the setting ± 3.45 V. The time delay can be measured at a larger deviation than the minimum required to operate the protection. The No trip tests need to be carried out at the setting ± 4 V and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

8.Protection – Loss of Mains test: For PV Inverters shall be tested in accordance with BS EN 62116. Other Inverters should be tested in accordance with EN 50438 Annex D.2.5 at 10%, 55% and 100% of rated power.

To be carried out at three output power levels with a tolerance of plus or minus 5% in Test Power levels.

Test Power	10%	55%	100%	10%	55%	100%
Balancing load on islanded network	95% of Registered Capacity	95% of Registered Capacity	95% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity
Trip time. Limit is 0.5 s	0.309s	0.3338s	0.397s	0.327s	0.351s	0.417s

For Multi phase **Micro-generators** confirm that the device shuts down correctly after the removal of a single fuse as well as operation of all phases.

Test Power	10%	55%	100%	10%	55%	100%
Balancing load on islanded network	95% of Registered Capacity	95% of Registered Capacity	95% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity
Trip time. Ph1 fuse removed	/	/	/	/	/	/
Test Power	10%	55%	100%	10%	55%	100%
Balancing load on islanded network	95% of Registered Capacity	95% of Registered Capacity	95% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity
Trip time. Ph2 fuse removed	/	/	/	/	/	/



Test Power	10%		55%	100%	0% 10%		6		55%	100%
Balancing load on islanded network	95% of Register Capacity		95% of Registered Capacity	Regis	egistered F		105% of Registered Capacity		105% of Registered Capacity	105% of Registered Capacity
Trip time. Ph3 fuse removed	/		/	/		/			1	1
Note for technologies which have a substantial shut down time this can be added to the 0.5 s in establishing that the trip occurred in less than 0.5 s. Maximum shut down time could therefore be up to 1.0 s for these technologies.										
Indicate additional	shut dov	vn tim	e included in	above r	esults.				40ms	
For Inverters test table.	ted to BS	EN 6	2116 the follo	owing s	ub set c	of test	ts sh	ould	be recorded in	n the following
Test Power and	33%		66%	100%	6	33%	6		66%	100%
imbalance	-5% Q		-5% Q	-5%	Р	+5%	6 Q		+5% Q	+5% P
	Test 22		Test 12	Test	5	Tes	Test 31		Test 21	Test 10
Trip time. Limit is 0.5 s	0.282s		0.311s	0.365	ōs	0.3	0.312s		0.341s	0.399s
	9.Protection – Frequency change, Vector Shift Stability test: This test should be carried out in accordance with EREC G98/NI Annex A1 A.1.2.6 (Inverter connected) or Annex A2 A.2.2.6 (Synchronous).									
		Start	Frequency	Chang	hange Confirm no trip					
Positive Vector Sh	nift	49.5	Hz	+50 de	egrees	rees No Trip				
Negative Vector S	hift	50.5	Hz	- 50 de	egrees	No Trip				
10.Protection – Frequency change, RoCoF Stability test: The requirement is specified in section 11.3, test procedure in Annex A.1.2.6 (Inverter connected) or Annex A2 A.2.2.6 (Synchronous).										
Ramp range		Test	frequency rai	mp:	Test Duration Confirm no trip					
49.0 Hz to 51.0 Hz	Z	+0.9	5 Hzs ⁻¹		2.1 s No Trip					
51.0 Hz to 49.0 Hz	Z	-0.95	5 Hzs ⁻¹		2.1 s	1 s No Trip				
11.Limited Frequence accordance with E out using the speci	EN 50438	Anne	x D.3.3 Powe	r respo	nse to o	ver-	frequ	iency		
Test sequence at Registered Capacity >80%		Ac	easured tive Power itput		uency	Prin	Primary Power Source Active Power			



3619.33W	50.00Hz	3681.33W	-
3527.16W	50.26Hz		-
2723.69W	50.69Hz		-
1914.52W	51.15Hz		-
2728.10W	50.70Hz		-
3523.44W	50.24Hz		-
3615.32W	50.00Hz		
Measured Active Power Output	Frequency	Primary Power Source	Active Power
Output			Gradient
1808.42W	50.00Hz	1856.23W	- Gradient
·	50.00Hz 50.26Hz	1856.23W	
1808.42W		1856.23W	
1808.42W 1727.38W	50.26Hz	1856.23W	
1808.42W 1727.38W 913.81W	50.26Hz 50.69Hz	1856.23W	
1808.42W 1727.38W 913.81W 115.66W	50.26Hz 50.69Hz 51.15Hz	1856.23W	
	3527.16W 2723.69W 1914.52W 2728.10W 3523.44W 3615.32W Measured Active Power	3527.16W 50.26Hz 2723.69W 50.69Hz 1914.52W 51.15Hz 2728.10W 50.70Hz 3523.44W 50.24Hz 3615.32W 50.00Hz Measured Active Power	3527.16W 50.26Hz 2723.69W 50.69Hz 1914.52W 51.15Hz 2728.10W 50.70Hz 3523.44W 50.24Hz 3615.32W 50.00Hz Measured Active Power Frequency Primary Power Source

Steps as defined in EN 50438

12.Power output with falling frequency test: This test should be carried out in accordance with EN 50438 Annex D.3.2 active power feed-in at under-frequency.

Test sequence	Measured Active Power Output	Frequency	Primary power source
Test a) 50 Hz ± 0.01 Hz	3617.52W	50.00Hz	3692.03W
Test b) Point between 49.5 Hz and 49.6 Hz	3605.41W	49.56Hz	3681.25W
Test c) Point between 47.5 Hz and 47.6 Hz	3594.13W	47.56Hz	3669.37W

NOTE: The operating point in Test (b) and (c) shall be maintained for at least 5 minutes

13.Re-connection timer.



Test should prove that the reconnection sequence starts after a minimum delay of 60 s for restoration of voltage and frequency to within the stage 1 settings of Table 2.										
Time delay setting	Measured delay			Checks on no reconnection when voltage or frequency is brought to ust outside stage 1 limits of table 2.						
60S	60S 70S			V	At 191	1.5 V	At 4	7.9 Hz	At 52.1 Hz	
	n that the does not re-con	Micro- nect.	Yes		Yes		Yes	;	Yes ith EREC G98/NI Annex Amps	
	vel contributio (Inverter conne							dance with ER	EC G98/NI Annex	
For machine	es with electro-r	magnet	ic output			For Inver	ter o	utput		
Parameter			Symbol	Va	llue	Time fault	after	Volts	Amps	
Peak Short Circuit current			ĺρ			20 ms		79.3V	28.1A	
Initial Value of aperiodic current		rrent	Α			100 ms		76.7V	21.4A	
Initial symmetrical short-circuit current*		cuit	I_k			250 ms		75.1V	15.9A	
Decaying (aperiodic) component of short circuit current*			i _{DC}			500 ms		72.9V	7.8A	
Reactance/Resistance Ratio of source*		X/ _R			Time to to	rip	0.225	In seconds		
circuit curre	For rotating machines and linear piston machines the test should produce a 0 s – 2 s plot of the short circuit current as seen at the Micro-generator terminals. * Values for these parameters should be provided where the short circuit duration is sufficiently long to									

^{*} Values for these parameters should be provided where the short circuit duration is sufficiently long to enable interpolation of the plot

15.Logic Interface.	Yes

This equipment is equipped with RJ45 terminal for logic interface that being received the signal from the DNO, the connection should be installed per installation manual, and the signal should be a simple binary output that captured by RJ45 terminal(PIN 5 and 1 for detecting the signal). Once the signal actived, the inverter will reduce its active power to zero within 5s.

16.Self-Monitoring solid state switching: No specified test requirements. Refer to EREC G98 Annex A1 A.1.3.6 (Inverter connected).	Yes/or NA
It has been verified that in the event of the solid state switching device failing to disconnect the Micro-generator , the voltage on the output side of the switching device is reduced to a value below 50 V within 0.5 s.	NA



Additional comments