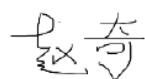


G98 Type Verification Test Report

<p>Type Approval and Manufacturer declaration of compliance with the requirements of G98. This form should be used when making a Type Test submission to the Energy Networks Association (ENA). If the Micro-generator is Fully Type Tested and already registered with the ENA Type Test Verification Report Register, the Installation Document should include the Manufacturer's Reference Number (the Product ID), and this form does not need to be submitted. Where the Micro-generator is not registered with the ENA Type Test Verification Report Register this form needs to be completed and provided to the DNO, to confirm that the Microgenerator has been tested to satisfy the requirements of this EREC G98.</p>			
SSEG Type reference number		MI-1200, MI-1000	
SSEG Type		Photovoltaic Microinverter	
System Supplier name		Hoymiles Converter Technology Co., Ltd.	
Address		No.18 Kangjing road, Hangzhou, Zhejiang Province, P.R. China	
Tel	+86 15088682210	Fax	+86 571 28056137
E:mail	zhangxingyao@hzconverter.com	Web site	www.hoymiles.com
Maximum rated capacity, use separate sheet if more than one connection option.	Connection Option		
	1.2/1.0	kW single phase, single, split or three phase system	
	NA	kW three phase	
	NA	kW two phases in three phase system	
	NA	kW two phases split phase system	
SSEG manufacturer/supplier declaration			
<p>Manufacturer Type Test declaration. - I certify that all products supplied by the company with the above Type Tested reference number will be manufactured and tested to ensure that they perform as stated in this document, prior to shipment to site and that no site modifications are required to ensure that the product meets all the requirements of EREC G98.</p>			
Signed		On behalf of	Hoymiles Converter Technology Co., Ltd

Note that testing can be done by the Manufacturer of an individual component or by an external test house.

Where parts of the testing are carried out by persons or organisations other than the Manufacturer then that person or organisation shall keep copies of all test records and results supplied to them to verify that the testing has been carried out by people with sufficient technical competency to carry out the tests.

Operating Range			
	Test 1	Test 2	Test 3
MI-1200	<u>195.5 V,47.5 Hz</u>	<u>253 V,51.5 Hz</u>	<u>253 V,52.0 Hz</u>
MI-1000			

Power Quality – Harmonics: These tests should be carried out as specified in BS EN 61000-3-2.						
SSEG rating per phase (rpp)			1.2	kW	NV=MV*3.68/rpp	
Harm onic	At 45-55% of rated output		100% of rated output		Limit in BS EN 61000-3- 2 in Amps	Higher limit for odd harmonics 21 and above
	Measured Value(MV) in Amps	Normalised Value (NV) In Amps	Measured Value(MV) In Amps	Normalised Value (NV) In Amps		
2	0.0182	0.0558	0.0375	0.1150	1.080	
3	0.0108	0.0331	0.033	0.1012	2.300	
4	0.0079	0.0242	0.0121	0.0371	0.430	
5	0.0135	0.0414	0.0435	0.1334	1.140	
6	0.0051	0.0156	0.0091	0.0279	0.300	
7	0.0193	0.0592	0.0371	0.1138	0.770	
8	0.0046	0.0141	0.0061	0.0187	0.230	
9	0.0142	0.0435	0.0373	0.1144	0.400	
10	0.0033	0.0101	0.0051	0.0156	0.184	
11	0.0074	0.0227	0.0407	0.1248	0.450	
12	0.0023	0.0071	0.0042	0.0129	0.153	
13	0.0071	0.0218	0.032	0.0981	0.210	

14	0.002	0.0061	0.0089	0.0273	0.131	
15	0.0054	0.0166	0.0241	0.0739	0.150	
16	0.0017	0.0052	0.0073	0.0224	0.115	
17	0.0081	0.0248	0.0175	0.0537	0.132	
18	0.0023	0.0071	0.0089	0.0273	0.102	
19	0.005	0.0153	0.0135	0.0414	0.118	
20	0.0023	0.0071	0.0044	0.0135	0.092	
21	0.0061	0.0187	0.0122	0.0374	0.107	
22	0.0014	0.0043	0.0071	0.0218	0.084	
23	0.0082	0.0251	0.0142	0.0435	0.098	0.147
24	0.0022	0.0067	0.0072	0.0221	0.077	
25	0.0061	0.0187	0.0174	0.0534	0.090	0.135
26	0.0026	0.0080	0.0085	0.0261	0.071	
27	0.0041	0.0126	0.0168	0.0515	0.083	0.124
28	0.0004	0.0012	0.0067	0.0205	0.066	
29	0.0038	0.0117	0.0133	0.0408	0.078	0.117
30	0.0021	0.0064	0.0101	0.0310	0.061	
31	0.0027	0.0083	0.0118	0.0362	0.073	0.109
32	0.002	0.0061	0.0104	0.0319	0.058	
33	0.0066	0.0202	0.0127	0.0389	0.068	0.102
34	0.0006	0.0018	0.0066	0.0202	0.054	
35	0.0077	0.0236	0.0112	0.0343	0.064	0.096
36	0.001	0.0031	0.0071	0.0218	0.051	
37	0.0048	0.0147	0.0078	0.0239	0.061	0.091
38	0.0017	0.0052	0.0092	0.0282	0.048	
39	0.0064	0.0196	0.008	0.0245	0.058	0.087
40	0.0005	0.0015	0.0091	0.0279	0.046	

Note the higher limits for odd harmonics 21 and above are only allowable under certain conditions, if these higher limits are utilised please state the exemption used as detailed in part 6.2.3.4 of BS EN 61000-3-2 in the box below.

Power Quality. Voltage fluctuations and Flicker.								
	Starting			Stopping			Running	
	dmax [%]	dc [%]	d(t) [%]	dmax [%]	dc [%]	d(t) [%]	Pst	Plt 2 hours
Measured Values	0.1	0	0	0.1	0	0	0.064	0.064
Normalised to standard impedance and 3.68kW for multiple units	0.3	0	0	0.3	0	0	0.2	0.2
Limits set under BS EN 61000-3-2	4%	3.30%	3.3% 500ms	4%	3.30%	3.3% 500ms	1	0.65
Test start date	2017-08-20			Test end date	2017-08-20			
Test location	No.8 Chunxin East Road, Wuxi, Jiangsu							

Power quality – DC injection: This test should be carried out in accordance with EN 50438 Annex D.3.10				
Test power level	20%	50%	75%	100%
Recorded value(mA)	0.262	1.03	1.74	1.35
as % of rated AC	0.025%	0.039%	0.044%	0.026%
Limit	0.25%	0.25%	0.25%	0.25%

Power Quality – Power factor: This test shall be carried out in accordance with EN 50538 Annex D.3.4.1 but with nominal voltage -6% and +10%. Voltage to be maintained within $\pm 1.5\%$ of the stated level during the test.

	216.2V	230V	253V	
20% of Registered	0.9910	0.9895	0.9864	
50% of Registered	0.9965	0.9961	0.9954	
75% of Registered	0.9974	0.9972	0.9968	
100% of Registered	0.9979	0.9977	0.9974	
Limit	>0.95	>0.95	>0.95	

Protection. Frequency tests These tests should be carried out in accordance with EN 50438 Annex D.2.4 and the notes in EREC G98 Annex A1 A 1.3.2 (Inverter connected) or Annex A2 A.2.2.2 (Synchronous)

Function	Setting		Trip test		"No trip tests"	
	Frequency	Time delay	Frequency	Time delay	Frequency /time	Confirm no trip
U/F stage 1	47.5Hz	20s	47.5Hz	20.1s	47.7Hz/ 25s	No trip
U/F stage 2	47Hz	0.5s	47Hz	0.54s	47.2Hz/ 19.98s	No trip
					46.8Hz/ 0.48s	No trip
O/F stage 2	52Hz	0.5s	52Hz	0.52s	51.8Hz/ 89.98s	No trip
					52.2Hz/ 0.48s	No trip

Protection. Voltage tests These tests should be carried out in accordance with EN 50438 Annex D.2.3 and the notes in EREC G98 Annex A1 A 1.3.1 (Inverter connected) or Annex A2 A.2.2.1 (Synchronous)

Function	Setting		Trip test		"No trip tests"	
	Voltage	Time delay	Voltage	Time delay	Voltage /time	Confirm no trip
U/V stage 2	184V	2.5s	183.4V	2.53s	188V/3.5s	No trip
					180V/2.48s	No trip
O/V stage 1	262.2V	1.0s	263.2V	1.02s	258.2V/2.0s	No trip
O/V stage 2	273.7V	0.5s	274.4V	0.52s	269.7V/0.98s	No trip

	277.7V/0.48s	No trip
<p>Note for Voltage tests the Voltage required to trip is the setting ± 3.45 V. The time delay can be measured at a larger deviation than the minimum required to operate the protection. The No trip tests need to be carried out at the setting ± 4 V and for the relevant times as shown in the table above to ensure that the protection will not trip in error.</p>		

<p>Protection. Loss of Mains test. For PV Inverters shall be tested in accordance with BS EN62116. Other Inverters should be tested in accordance with EN 50438 Annex D.2.5 at 10%,55% and 100% of rated power.</p>						
<p>Note: Inverter tested according to BS EN 62116.</p>						
Test Power and imbalance	33% -5% Q	66% -5% Q	100% -5% P	33% +5% Q	66% +5% Q	100% +5% P
Trip time. Limit is 0.5s	82.5ms	173.6ms	227.4ms	84.8ms	171.3ms	237.8ms

<p>Protection. Frequency change, Stability test This test should be carried out in accordance with EREC G98 Annex A1 A 1.3.5 (Inverter connected) or Annex A2 A.2.2.5 (Synchronous).</p>				
	Start Frequency	Change	End Frequency	Confirm no trip
Positive Vector Shift	49.5Hz	+50 degrees		No trip
Negative Vector Shift	50.5Hz	- 50degrees		No trip
<p>Protection – Frequency change, RoCoF Stability test: The requirement is specified in section 11.3, test procedure in Annex A 1.3.5 (Inverter connected) or Annex A2 A.2.2.5 (Synchronous).</p>				
	Ramp range	Test frequency	Test Duration	Confirm no trip
Positive Frequency drift	49Hz to 51Hz	+0.95Hz/sec	2.1s	No trip
Negative Frequency drift	51Hz to 49Hz	-0.95Hz/sec	2.1s	No trip

<p>Protection – Limited Frequency Sensitive Mode – Overfrequency test: This test should be carried out in accordance with EN 50438 Annex D.3.3 Power response to over- frequency. The test should be carried out using the specific threshold frequency of 50.4 Hz and dro op of 10%.</p>				
Test sequence at Registered Capacity >80%	Measured Active Power Output	Frequency	Primary Power Source	Active Power Gradient
Step a) 50.00	1199.4W	50Hz		-

Hz ± 0.01 Hz				
Step b) 50.45 Hz ± 0.05 Hz	1193.2W	50.45Hz		-
Step c) 50.70 Hz ± 0.10 Hz	1162.6W	50.7Hz		-
Step d) 51.15 Hz ± 0.05 Hz	1108.5W	51.15Hz		-
Step e) 50.70 Hz ± 0.10 Hz	1162.8W	50.7Hz		-
Step f) 50.45 Hz ± 0.05 Hz	1192.8W	50.45Hz		-
Step g) 50.00 Hz ± 0.10 Hz	1198.6W	50Hz		-
Test sequence at Registered Capacity 40% - 60%	Measured Active Power Output	Frequency	Primary Power Source	Active Power Gradient
Step a) 50.00 Hz ± 0.01 Hz	613.2W	50Hz		-
Step b) 50.45 Hz ± 0.05 Hz	609.6W	50.45Hz		-
Step c) 50.70 Hz ± 0.10 Hz	594.7W	50.7Hz		-
Step d) 51.15 Hz ± 0.05 Hz	567.3W	51.15Hz		-
Step e) 50.70 Hz ± 0.10 Hz	593.5W	50.7Hz		-
Step f) 50.45 Hz ± 0.05 Hz	609.8W	50.45Hz		-
Step g) 50.00 Hz ± 0.10 Hz	613.3W	50Hz		-
Steps as defined in EN 50438				

Protection – Power output with falling frequency test: This test should be carried out in accordance with EN 50438 Annex D.3.2 active power feed -in at under-frequency.			
Test sequence	Measured Active Power Output	Frequency	Primary power source
Test a) 50 Hz ± 0.01 Hz	1198.7W	50 Hz	
Test b) Point between 49.5 Hz and 49.6 Hz	1197.9W	49.55Hz	
Test c) Point between 47.5 Hz and 47.6 Hz	1197.8W	47.55Hz	
NOTE: The operating point in Test (b) and (c) shall be maintained for at least 5 minutes			

Protection. Re-connection timer.					
Test should prove that the reconnection sequence starts after a minimum delay of 20 s for restoration of voltage and frequency to within the stage 1 settings of Table 2.					
Time delay setting	Measured delay	No reconnection when voltage or frequency is brought to just outside stage 1 limits of table 1.			
40s	40.1s	At 266.2V	At 180V	At 47.4Hz	At 52.1Hz
Confirmation that the SSEG does not re-connect.		No reconnection	No reconnection	No reconnection	No reconnection

Fault level contribution. The requirement is specified in section 5.7, test procedure in Annex A or B 1.4.6					
For a directly coupled SSEG			For a Inverter SSEG		
Parameter	Symbol	Value	Time after fault	Volts	Amps
Peak Short Circuit current	i_p	N/A	20ms	17.3V	0.257A
Initial Value of aperiodic	A	N/A	100ms	6.68V	0.149A
Initial symmetrical	I_k	N/A	250ms	6.22V	0.138A
Decaying (aperiodic)	i_{DC}	N/A	500ms	5.59V	0.125A
Reactance/Resistance Ratio	X/R	N/A	Time to trip	0.0039s	(in seconds)

Self-Monitoring solid state switching :No specified test requirements. Refer to EREC G98 Annex A1 A 1.4.6 (Inverter connected).	Yes/or NA
It has been verified that in the event of the solid state switching device failing to disconnect the Micro-generator, the voltage on the output side of the switching device is reduced to a value below 50 V within 0.5 s.	N/A

Additional comments